

Class	Subclass
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PATENT NUMBER

JL2  
-J1809

## U.S. UTILITY Patent Application

m.H O.I.P.E. SCANNED <u>01 Jan 40</u> O.A. <u>40</u>	PATENT DATE
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APPLICATION NO. 09/636484	CONT/PRIOR D	CLASS 257	SUBCLASS 392	ART UNIT 2811	EXAMINER KANG <del>HOANG</del>
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TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. H. ...</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. H. ...</b>
3. <b>Secretary</b>	3. <b>Mr. J. H. ...</b>
4. <b>Treasurer</b>	4. <b>Mr. J. H. ...</b>
5. <b>Member</b>	5. <b>Mr. J. H. ...</b>
6. <b>Member</b>	6. <b>Mr. J. H. ...</b>
7. <b>Member</b>	7. <b>Mr. J. H. ...</b>
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93. <b>Member</b>	93. <b>Mr. J. H. ...</b>
94. <b>Member</b>	94. <b>Mr. J. H. ...</b>
95. <b>Member</b>	95. <b>Mr. J.</b>


Walter Braddock

Integrated transistor devices

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**12/99**

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CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
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